



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshiyuki UCHINONO, *et al.*  
Serial No.: 09/995,085  
Filed : November 27, 2001  
Title : MULTILAYER CIRCUIT BOARD AND METHOD OF MANUFACTURING  
THE SAME

Art Unit : 2827  
Examiner : DINH, Tuan T  
Confirmation No.: 9568

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR CONTINUED EXAMINATION SUBMISSION**

**UNDER 37 CFR § 1.114**

In response to the final Office Action dated March 10, 2004, please amend the application as follows and consider the included remarks.